

### Pellet target development

31.03.2020 Otari Javakhishvili





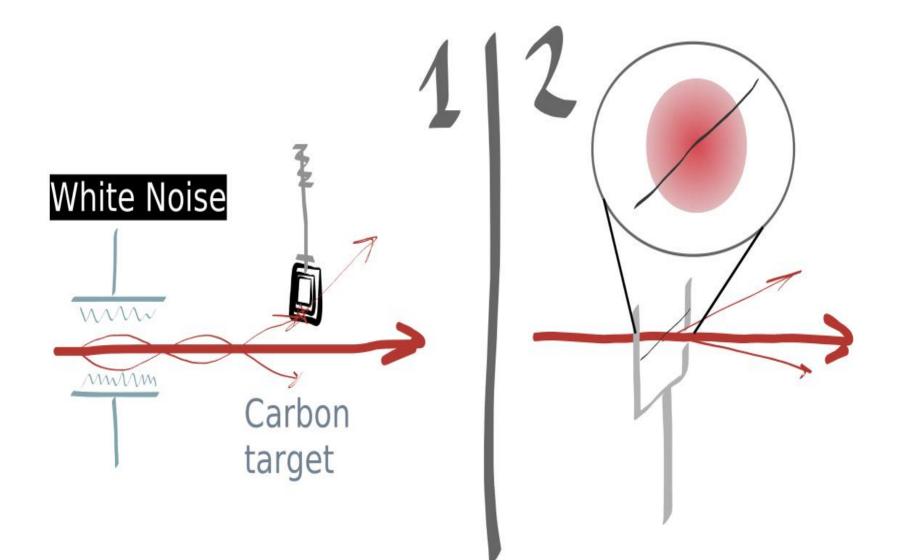


#### **Requirements**

- Minimal influence on the beam
- Minimal influence on vacuum system of the storage ring
- Frequency and speed of the target must be variable
- Must have precise triggering and TOF measurement
- Must be able to be synchronized with other parts of the detector



#### **Different Target Systems**

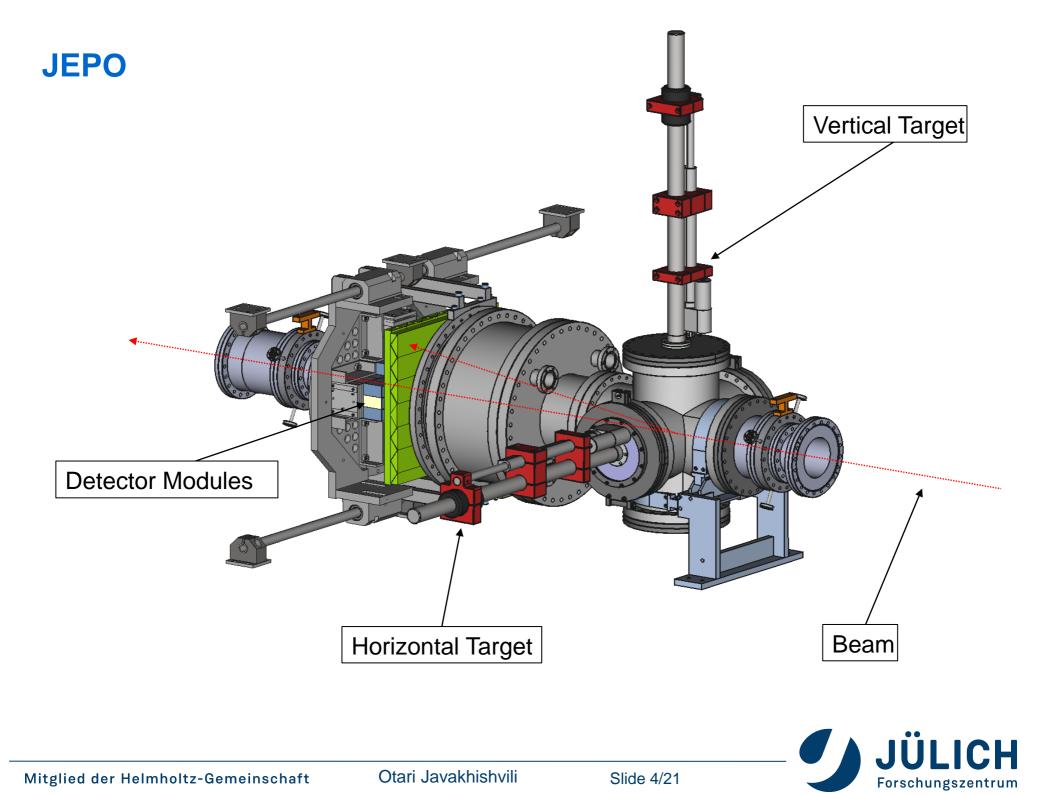




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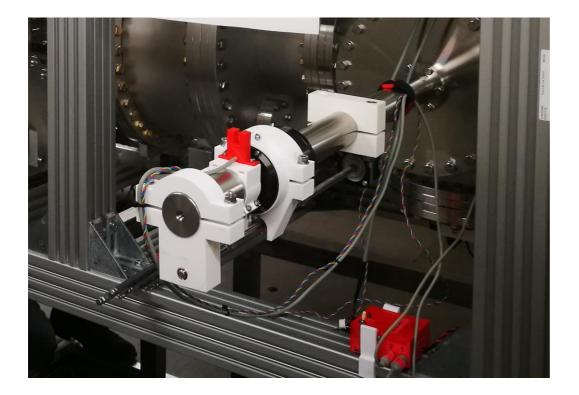
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#### **Block Target System**

- Horizontal and vertical targets
- Linear actuator using stepper motors
- Software and hardware interlock systems
- Industry standard instructions using G-Code





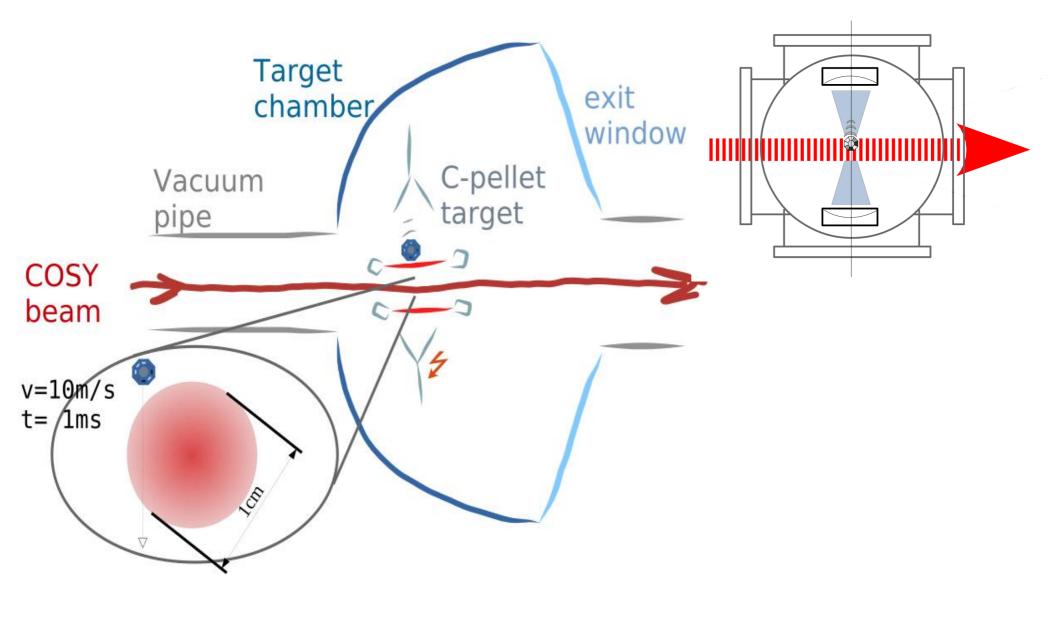


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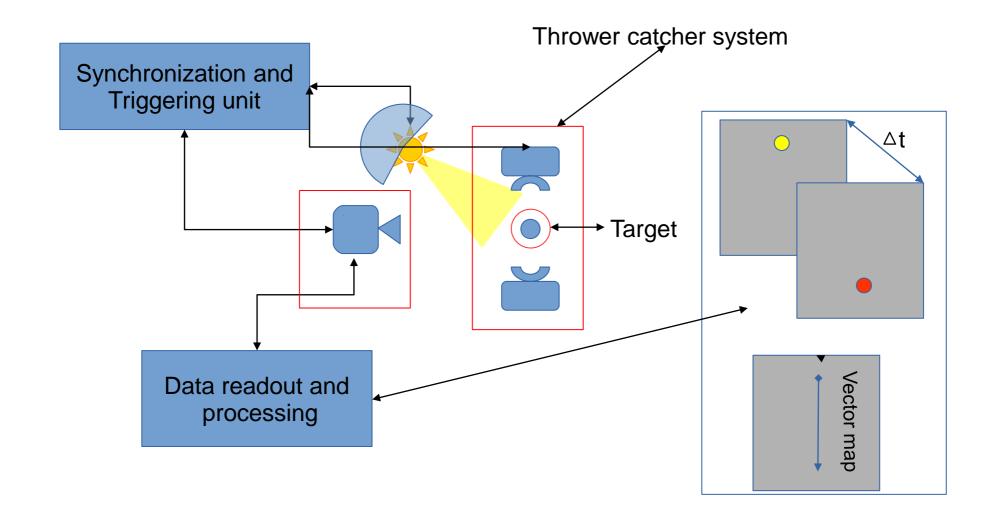
#### **Ballistic pellet target system**





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#### Pellet target system working diagram

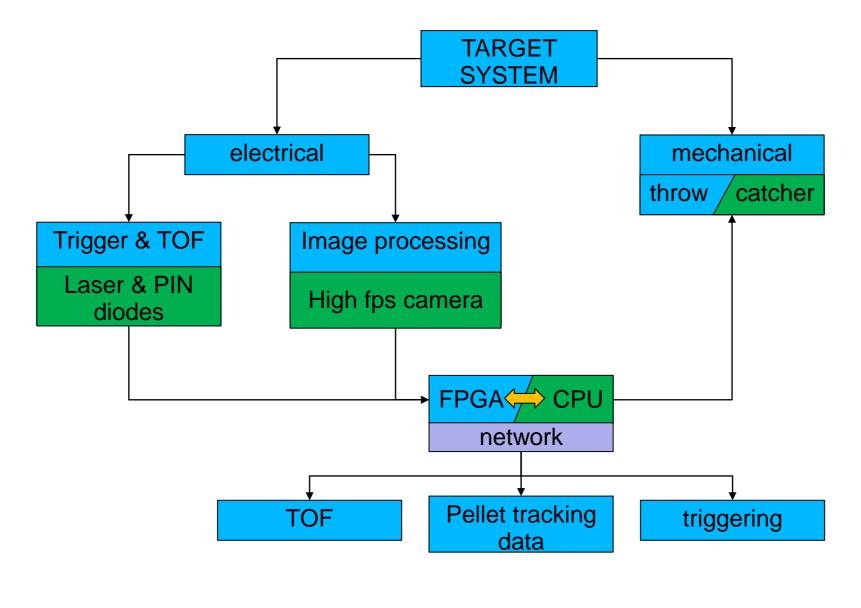




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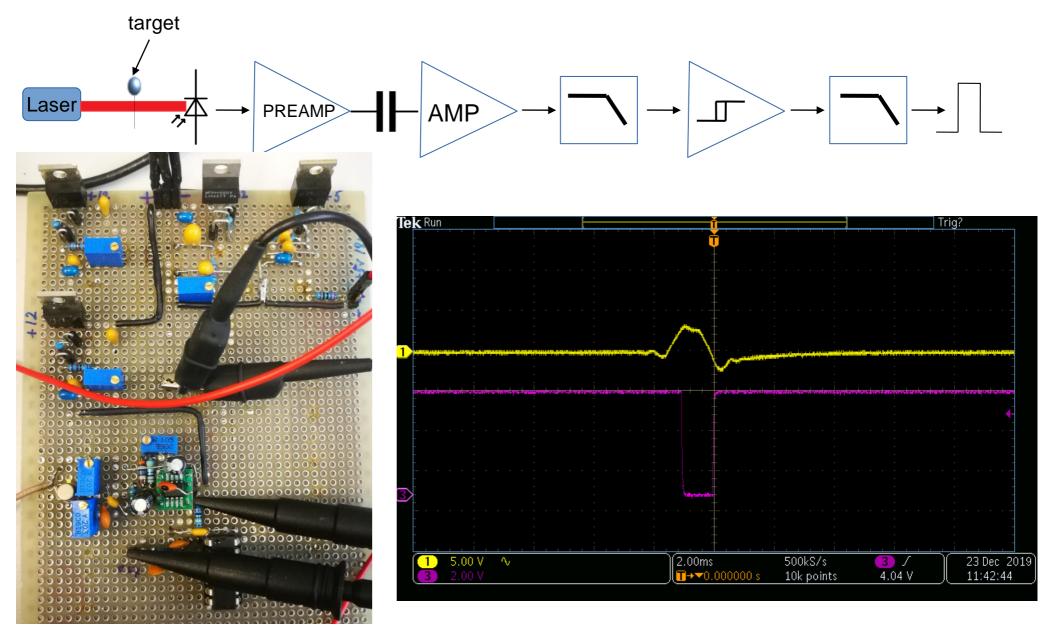
#### **SYSTEM DESIGN**





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#### **Triger system**



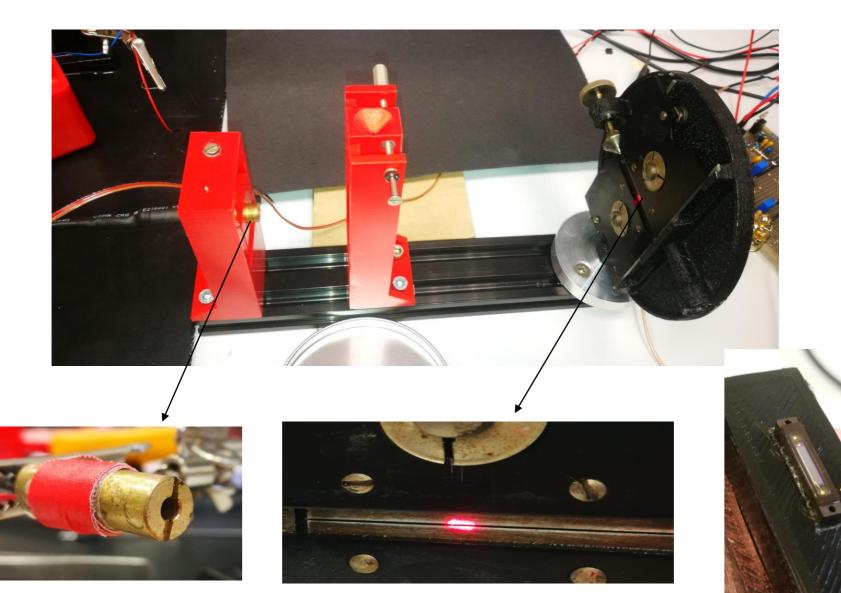


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#### **Triger system testbench**





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#### **FPGA Image processing**



5MP - CMOS image sensor

MIPI CSI-2 interface

Resolution - QSXGA@15Hz, 1080p@30Hz, 720p@60Hz, VGA@90Hz and QVGA@120Hz

Formats - RAW10, RGB565, CCIR656, YUV422/420, YCbCr422, and JPEG

667 MHz dual-core Cortex-A9 processor 1G Ethernet, USB 2.0, SDIO

1 GB DDR3L RAM

FPGA – XC7Z020-1CLG400C Look-up Tables (LUTs) 53,200 Flip-Flops 106,400

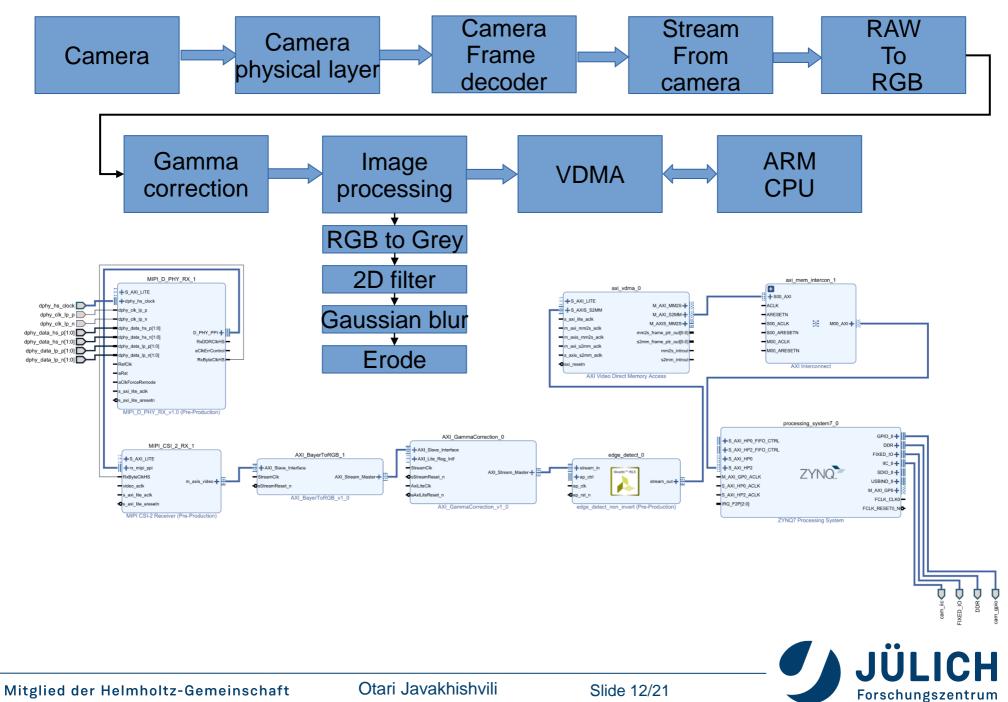


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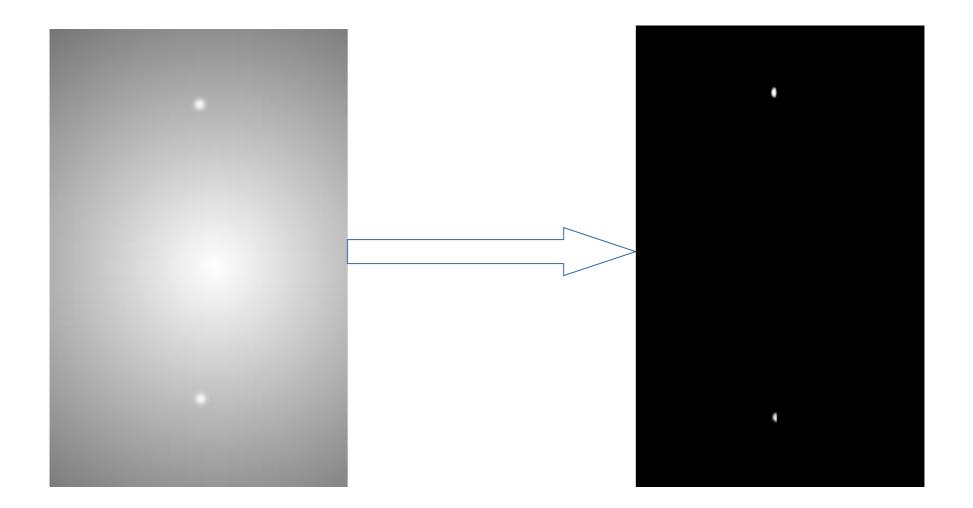
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#### **FPGA Image processing**



#### **Vivado HLS IP development**





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#### **Camera and image processing test**

Image from custom linux Running on FPGA board

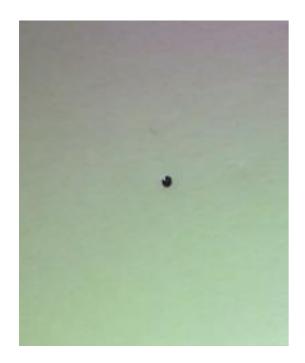
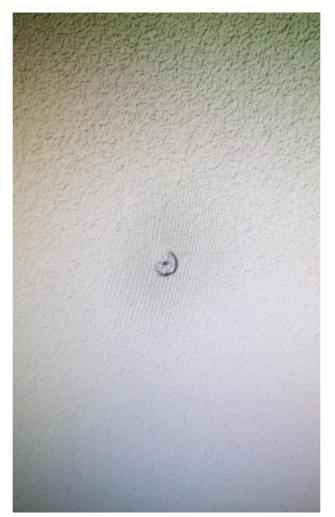
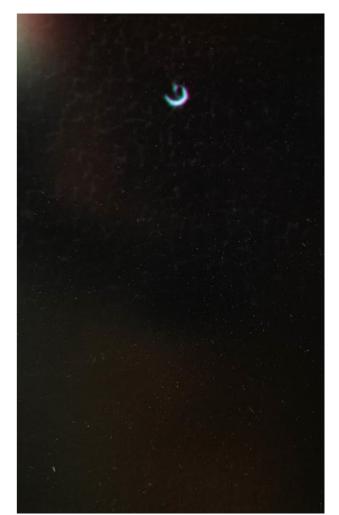


Image processing test using color invert and Sobel filter



#### Image processing test using Pewitt filter





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#### Summary

- First prototype of triggering system has ben developed and tested with 400 µm ball.
- First camera tests were made on FPGA
- Test image processing IP has been developed
- Custom Linux distro was built with working programming, network, GPIO and camera interfaces

#### Outlook

- Automatized mechanics should be developed for target tests (dropper/catcher system)
- Another trigger system must be assembled dedicated mechanical system should be developed for two trigger system to test TOF.
- AXI control interface should be implemented in image processing IP to control some parameters from CPU
- Developing a stroboscopic system for image capturing
- New system design must be created with image processing IP cores and implemented in Linux



# THANK YOU



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# Appendix

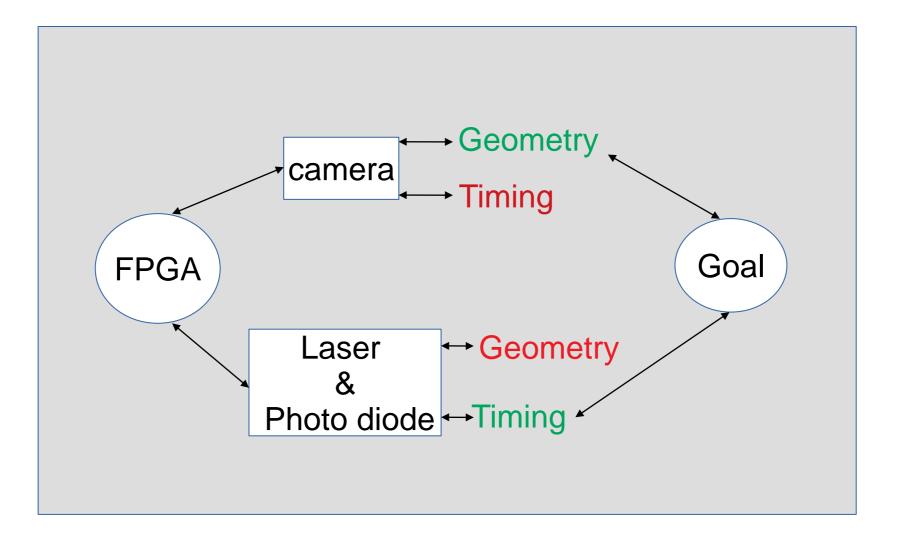


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#### ოპტიმალური სისტემების შერჩევა



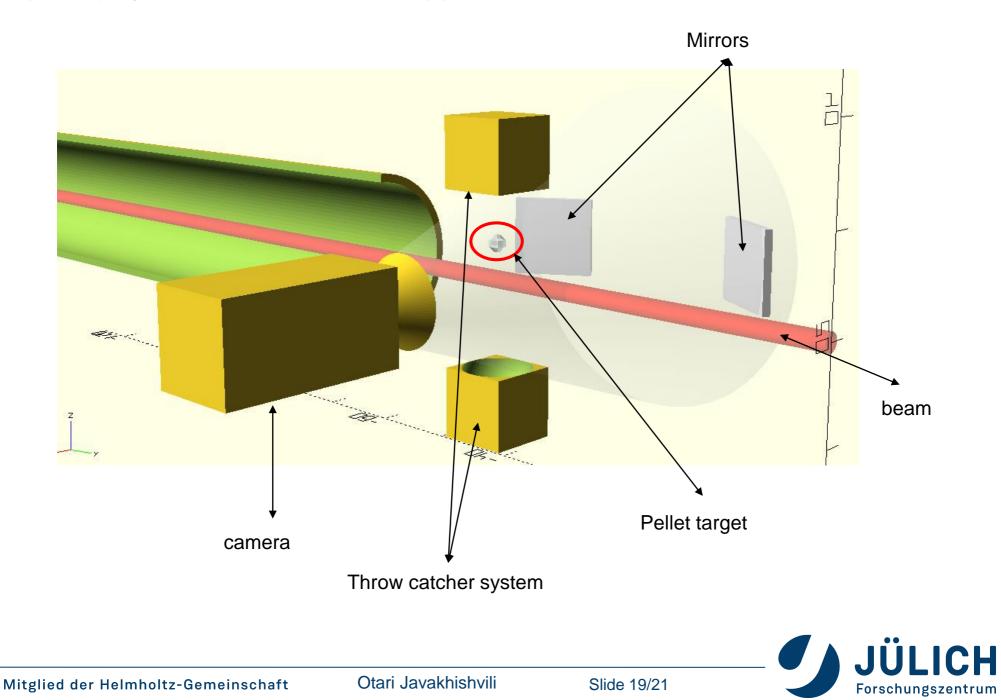


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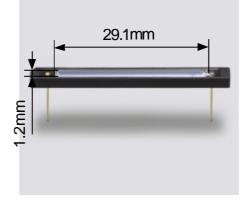
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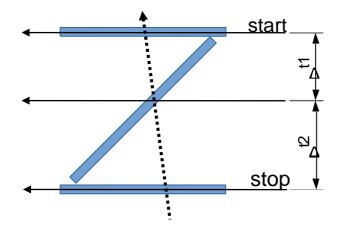
#### გრანულიანი სამიზნის 3D სქემა

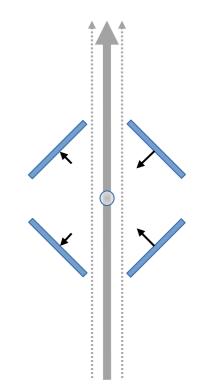


### სამიზნის ფრენის დროის გაზომვა

- Triggering
- Position reconstruction









#### **Camera requirements**

Minimum camera characteristics with window size 1x4 cm and pellet diameter 100µ:

- Minimum 26 fps to get 2 points (free fall)
- Minimum 400 pixels



Pco - pco.dimax HS4 Fps = 2277 @ 4Mpix (2000x2000) or 7039 @ 1MPix (1000x1000)

Interfaces: USB 3.0, GigE/USB 2.0, Camera Link

exposure time range 1.5 µs - 40 ms



Ximea - CB019MG-LX-X8G3

Fps = 2500+ @ 2Mpix (1920x1080)

Interfaces: PCI Express (PCIe) Gen3

exposure time range =  $1\mu s - 1sec$ 



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