

LYSO modules for the JEDI polarimeter: production, laboratory tests and results of first measurements

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Talk overview

Objective

- LYSO Module Assembling
- Module Inspection and Tuning in the Lab
- □ Energy Resolution & Linearity Tests Results

COSY Tests

□ Summary & Outlook

New concept of the JEDI Polarimeter



Goal: 10⁻²⁹ e cm



Polarimetry Overview



LYSO Modules Assembling







LYSO Modules Assembling





3rd hand during assembling

LYSO Modules Lab Tests





- Light Tightness
- Measurements of ²²Na, ⁶⁰Co, ¹⁷⁶Lu (internal)
- Optimal supply voltages
- Signal offset (current leakage)





LYSO Modules Lab Tests Analysis





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LYSO Modules Lab Tests Analysis







LYSO Modules Lab Tests Analysis





Saturation Test on LYSO & SiPM





Time Resolution Analysis



Struck innovation system: SIS3316 flash ADC



Features/Properties:

- 16 channels •
- 250 MS/s per channel = 4ns between timestamps ٠
- 14-bit resolution •
- 64 MSamples memory/channel ٠
- ٠ ...



Time

Signal

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Time Resolution Analysis (SW CFD)



Cosmic Run with PMT & LYSO



Time Resolution Analysis (HW CFD)



March 2017 beam time data

Deuteron TOF between start counter and detector



2nd Set-up for COSY Beam Time





Target wheel



 $A_{v}(\Theta) \quad dC \rightarrow pnC$







Summary

- LYSO module assembling and testing procedure
- ✓ Analyzing of the deuteron break-up reaction $dC \rightarrow pnC$
- ✓ CFD for higher time resolution (need more investigation for SiPMs)

Outlook

Getting ready for the next polarimetry beam time in Dec, 2017

- □ Assembling the new 46 modules and lab tests
- □ Upgrade HW/SW packages for the read out system
- Development of power supply with monitoring option

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Thank You





Appendix

1st Set-up for COSY Beam Time





First Test on LYSO – Bragg peak





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SiPM Power Supply Schematic





- Linear voltage regulator part
- Ramp generator and on/off part

Power Supply for SiPM







First prototype board on a test bench *(using internal voltage reference)*

Output on/off curve with voltage ramp

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SiPM Gain vs Bias Voltage



